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J, White
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)
Shinji TAKEDA et al.) Atty. Docket No.: **TM&K0008**
Serial No. 09/785,486) Group Art Unit: 2814
Filed: February 20, 2001) Examiner: GRAYBILL, D.
For: SEMICONDUCTOR DEVICE AND)
PROCESS FOR FABRICATION) Date: June 19, 2002
THEREOF)

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This is a Supplemental Information Disclosure Statement with regard to the above-identified application. Copies of each of the documents listed in the attached Form PTO/SB/08B are attached. These documents were cited in the corresponding Japanese Opposition and the remaining references cited in the Opposition, namely JP 6-218880, JP 6-264035, JP 6-145639, JP 7-22441, JP3117971, JP3117972 and JP3117966 have been already submitted with the Supplemental Information Disclosure Statement previously filed.

Document A is Figure 1 which is referred to Opposition paper, but is not believed to be prior art. Document B relates to surface energy. Document C relates to thermodynamic equilibrium of wetting. The partial translations for Documents B and C are attached to each document.

A complete copy of the Opposition paper is also enclosed. However, no translation is readily available.

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It is respectfully requested that the attached documents be considered and officially cited, and that a copy of the Form PTO/SB/08B, initialed by the Examiner to indicate that the documents have been considered, be returned to the applicants.

This information disclosure statement is being filed with the check covering the government fee of \$180.00. The Commissioner is hereby authorized to charge any fee deficiency or any additional fees which may be further required in this application, or credit any overpayment to Deposit Account No. 501281, under an order number which corresponds to the above attorney docket number.

Respectfully submitted,

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